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## \*BIBDATASHEET\*

CONFIRMATION NO. 6106

Bib Data Sheet

SERIAL NUMBER 10/721,110	FILING DATE 11/24/2003 RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. 2269-3437.9US (97- 0514.09)
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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 10/338,530 01/08/2003  
 which is a DIV of 09/819,472 03/28/2001 PAT 6,545,498  
 which is a DIV of 09/166,369 10/05/1998 PAT 6,329,832

*checked RE*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

*None RE*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/24/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY ID	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged	<i>RE</i>	<i>RE</i>	<i>RE</i>	Initials	5	12	9

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## TITLE

Method for in-line testing of flip-chip semiconductor assemblies

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees ( Filing )

FILING FEE FEES: Authority has been given in Paper

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1286

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